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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yuan-Ku Lan)
Serial No.: 10/086,928)
Filed: February 28, 2002) Our Ref: B-4514 619559-4
For: "METHOD OF CHECKING OVERLAP)
ACCURACY OF PATTERNS ON FOUR) Date: June 14, 2002
STACKED SEMICONDUCTOR LAYERS")

RECEIVED

JUN 27 2002

CLAIM TO PRIORITY UNDER 35 U.S.C. 119

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Technology Center 260

Sir:

[X] Applicant hereby makes a right of priority claim under 35 U.S.C. 119 for the benefit of the filing date(s) of the following corresponding foreign application(s):

<u>COUNTRY</u>	<u>FILING DATE</u>	<u>SERIAL NUMBER</u>
TAIWAN, R.O.C.	4 September 2001	90121880

[] A certified copy of each of the above-noted patent applications was filed with the Parent Application No. _____.

[X] To support applicant's claim, a certified copy of the above-identified foreign patent application is enclosed herewith.

[] The priority document will be forwarded to the Patent Office when required or prior to issuance.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to the "Commissioner of Patents and Trademarks, Washington, D.C. 20231", on June 14, 2002 by Courtney Comstock.

Courtney Comstock

Respectfully submitted,

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